

Neosera RHEiMS Evaluation Report:

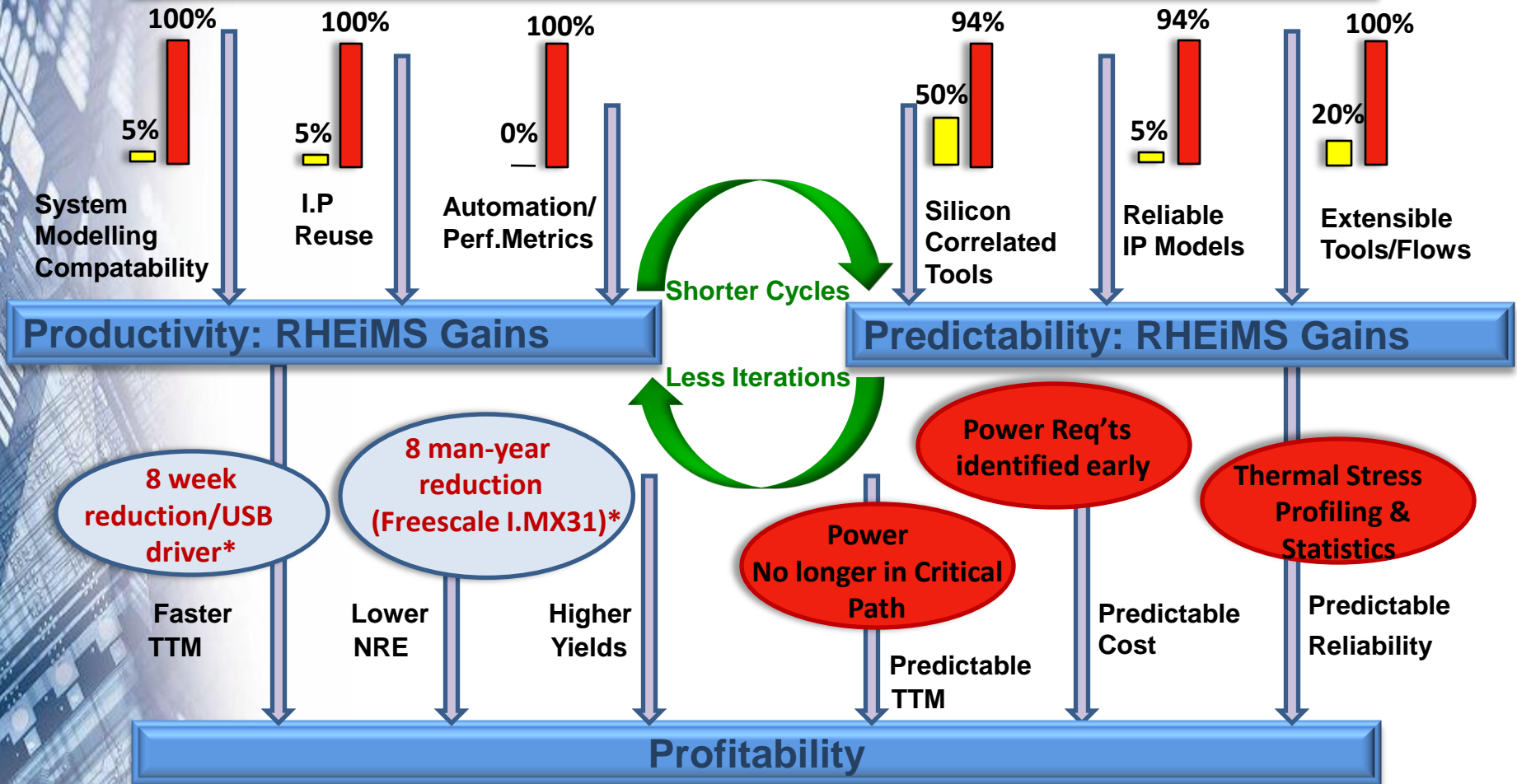
**A Study of the Performance, Productivity, Accuracy and Efficiency of Current
System-level power tools
compared to the RHEiMS System.**

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System-level Performance Comparison

Current Tools: Spreadsheets, State-machines, etc █ : RHEiMS: █



* (Synopsys) White Paper: Accelerating Software Driver Development using Virtual Platforms, August, 2008. The survey showed that companies such as Marvell, Freescale and TI enjoyed 85% first time s/ware portability on first time silicon by using System-level tools.

Unique Benefits of RHEiMS:

- One comprehensive power model from System to Gate-level, guarantees that power models are **consistent** and **accurate**. This provides greater **Precision** in modelling and greater determination in **Predicting** TTM.
- Power models are acquired from existing design-flows. **Increases Productivity and Faster TTM**.
- Cost and time **savings** are **substantial** and more than recovered in first project. Furthermore, savings are recurrent on every project.
- **RHEiMS** opens **new business** in line/product opportunities, such as power analysis in SDK of chip-sets for end-customers.

RHEiMS related tools and features:

- **RHEiMS-OPSL**: An extension tool to RHEiMS which takes voltage, frequency and other operational constraints, and determines the optimum operational conditions to minimise power.
- **RHEiMS-ISS**: An instruction set simulator which accepts embedded code, processor, cache structure and compiler optimisation details and rapidly generates a cycle accurate (<5% error) system-level estimation of performance.
- **RHEiMS-PRTL**: The RTL power estimation tool which uses the same power models generated for system-level applications